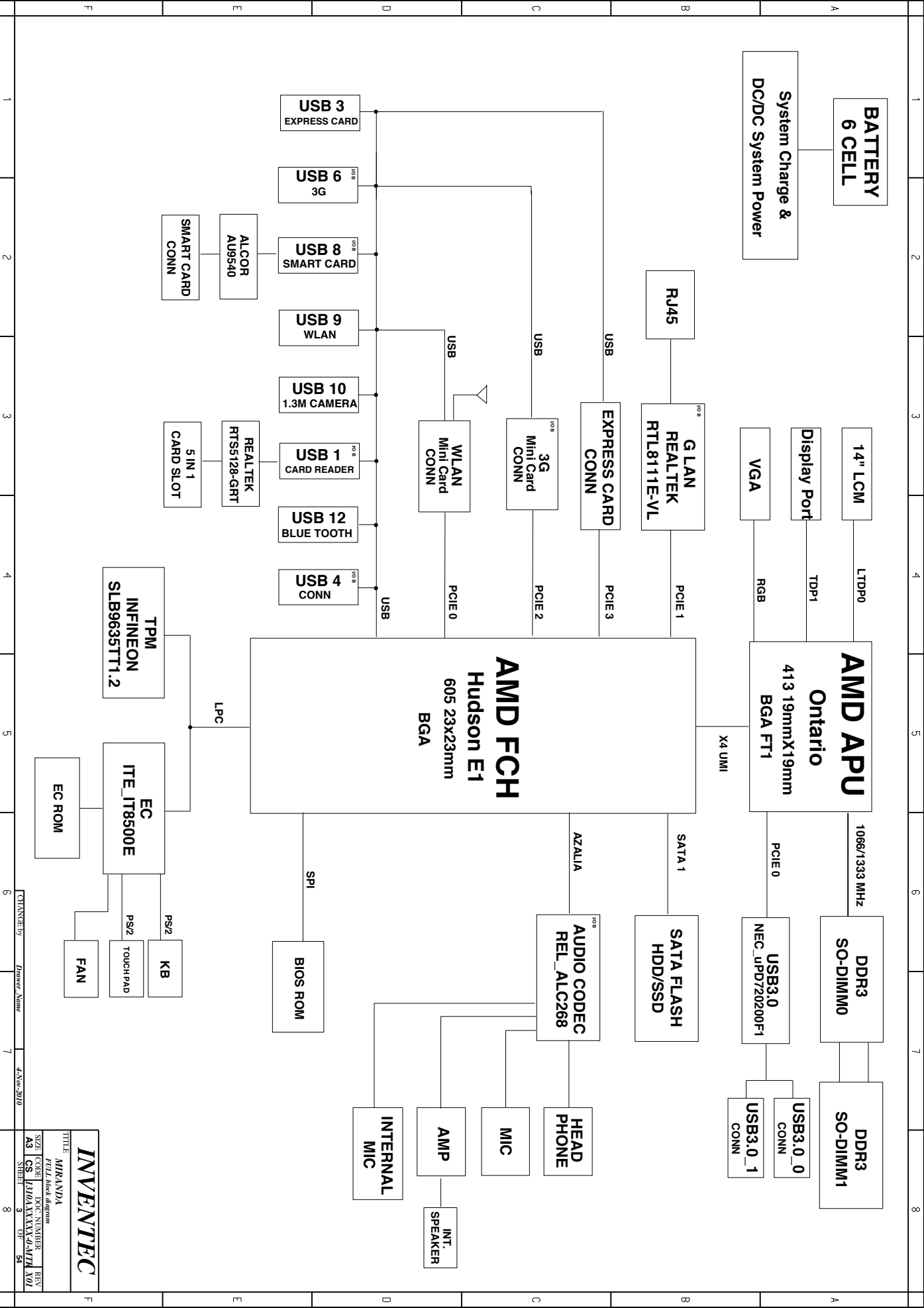


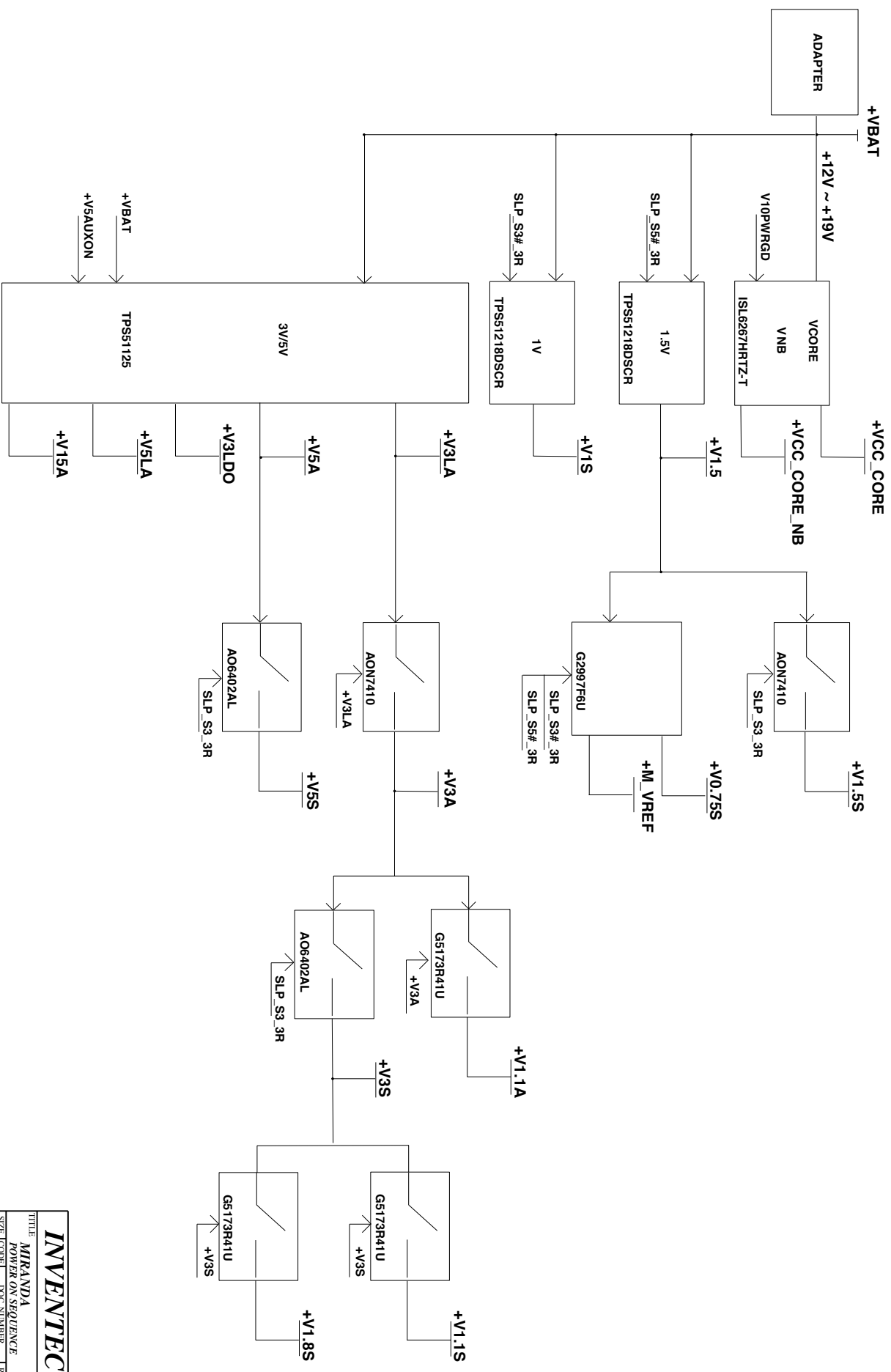
2010.11.26

[illegible]

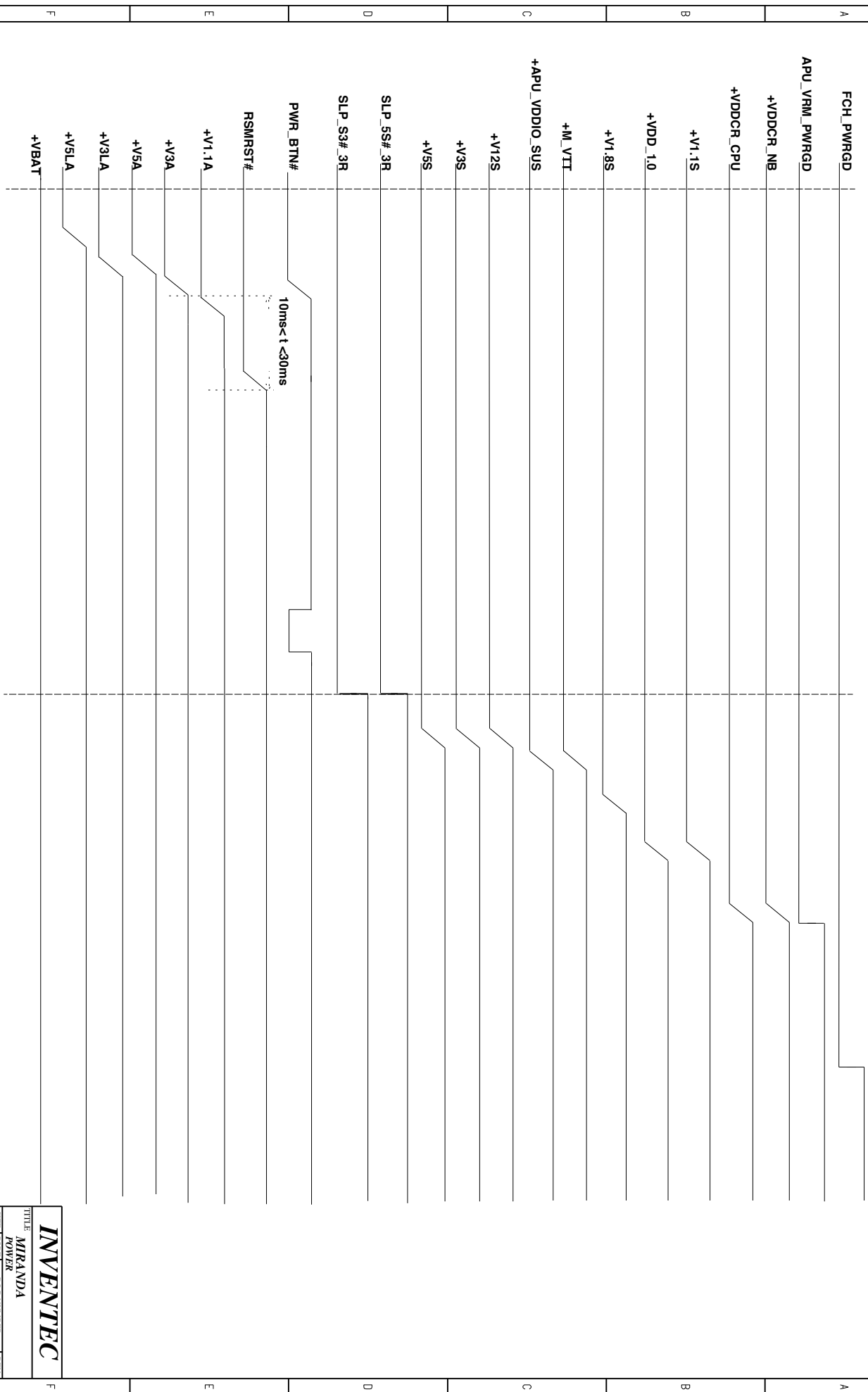
	1	2	3	4	5	6	7	8		
A	<div>TABLE OF CONTENTS</div>									
B	PAGE				PAGE					
	1. COVER PAGE				20. FCH1	I/O BOARD			B	
	2. INDEX				21. FCH2	40. CODEC REL_ALC268				
	3. BLOCK DIAGRAM				22. FCH3	41. AUDIO JACK				
	4. POWER BLOCK DAIGRAM				23. FCH4	42. AMP / INT. SPEAKER				
	5. POWER ON SEQUENCE				24. STRAP OPTION / THERMAL / RTC	43. WWAN				
	6. POWER ON SEQUENCE				25. LCM	44. SMART CARD AU9540			C	
	7. POWER CHARGE/DC IN				26. CRT	45. CARD READER				
	8. POWER BATTERY				27. DP	46. GIGA LAN				
	9. POWER +V3LA/+V3A/+V5A				28. WLAN / BT	47. RJ45				
	10. POWER +V1.8S/+V0.75S/+V1.5				29. TPM	48. USB CONN			D	
	11. POWER +V1.1A/+V1A/+V1.1S				30. SSD / HDD	49. WIRE to BOARD CONN (I/O/B SIDE)				
	12. POWER VCORE/CORE_NB				31. IT8500E	TP BOARD				
	13. POWER +V5S/+V3S/+V1.1S				32. KB/TP CONN	50. TP / HALL-SENSOR				
	14. APU FT1 1				33. USB3.0 CONTROLLER	POWER BOARD			E	
	15. APU FT1 2				34. USB3.0 CONN	51. POWER SW				
	16. APU FT1 3				35. SIM CARD	52. EMI				
	17. APU FT1 4				36. EXPRESS CARD					
	18. DDR3-1				37. FAN CONN					
	19. DDR3-2				38. WIRE to BOARD CONN (MB SIDE)					
					39. SCREWS					
F	1	2	3	4	5	6	7	8	F	
<div>CHANGED BYDrawn Name</div> <div>11-05-2010</div>								<div><div>INVENTEC</div><div><div>TITLE</div><div>MIRANDA</div></div><div><div>index</div><div><div>SIZE CODE</div><div>A3</div></div><div><div>DOC NUMBER</div><div>1310AXXX3-2-MIRK01</div></div><div><div>REV</div><div>1</div></div></div></div>		



Power Block Diagram



POWER ON SEQ.



1	2	3	4	5	6	7	8
1	2	3	4	5	6	7	8

CHANGED BY: MS 28-Oct-2010

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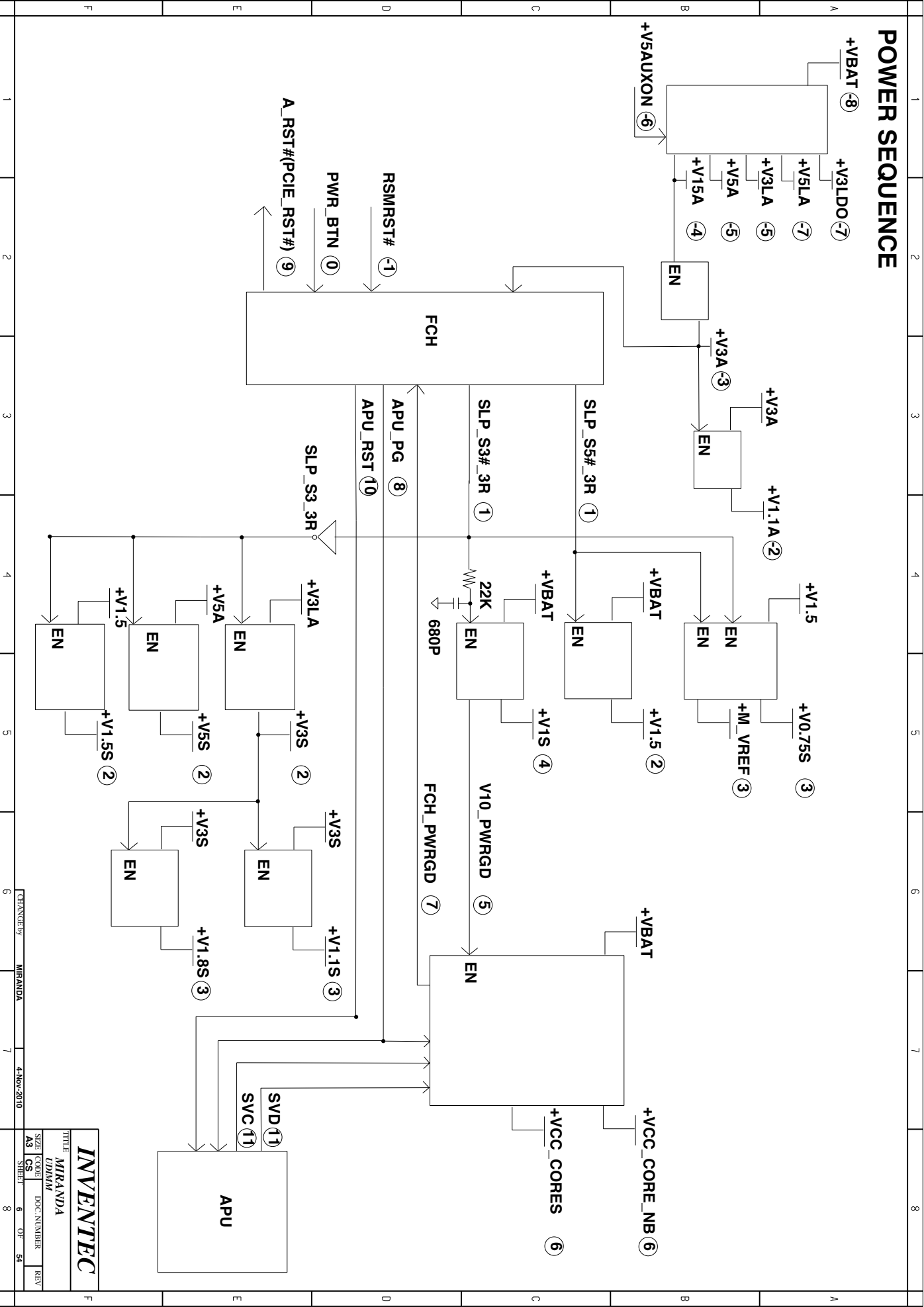
MIRANDA

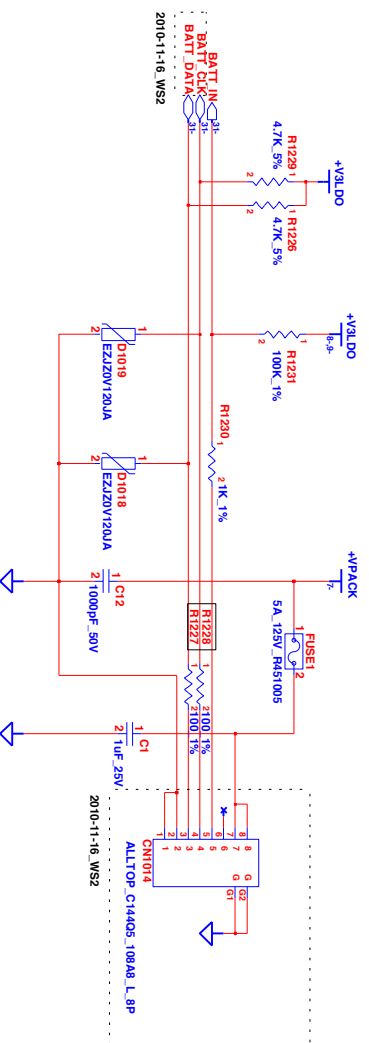
POWER

SIZE CODE: A3 CS1

DOC NUMBER: 8

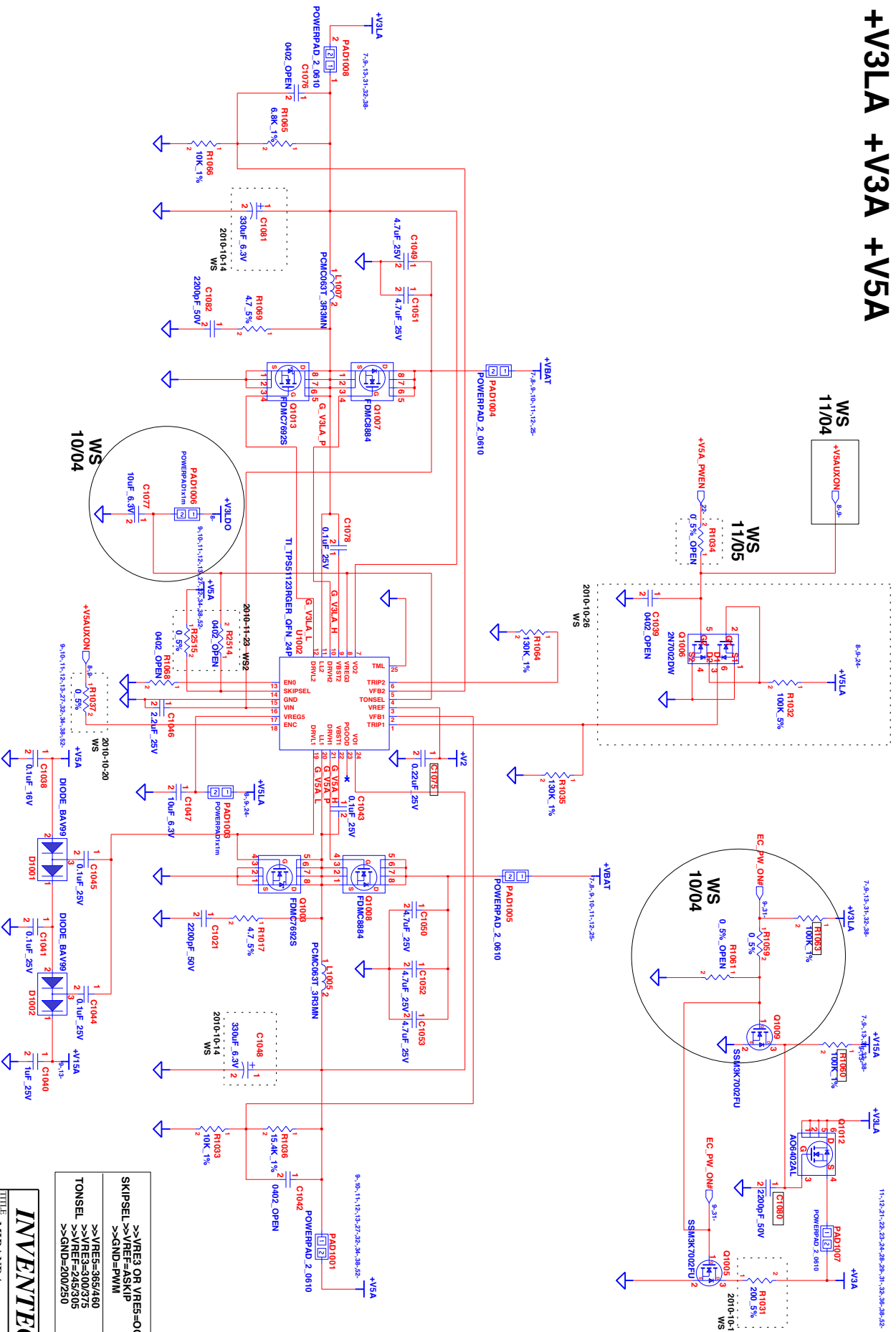
REV: 01 54





TITLE			
MIRANDA			
POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
SHEET		8	OF 54
		8	

+V3LA +V3A +V5A

**INVENTEC**

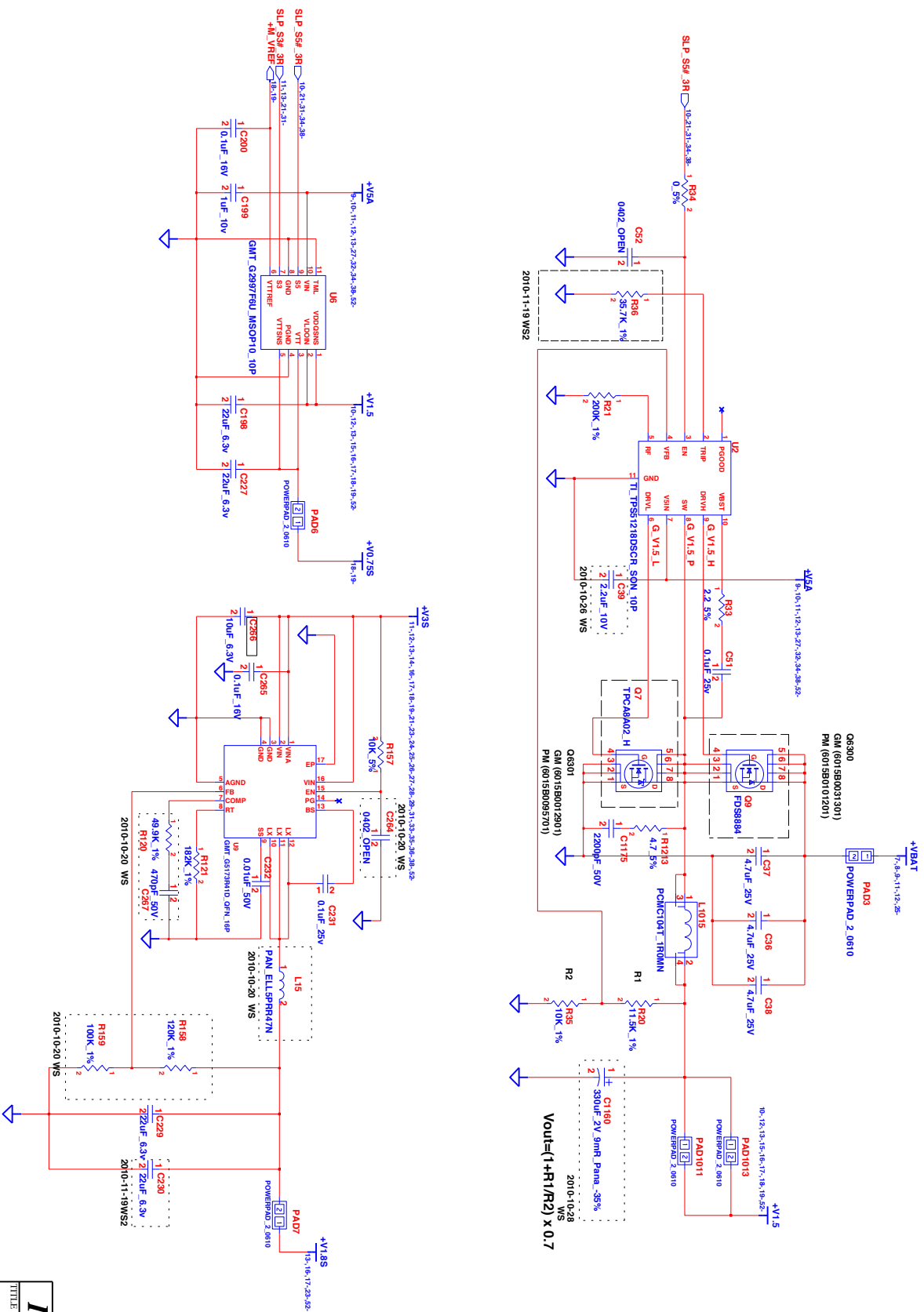
TITLE
MIRANDA

POWER.

SIZE	CODE
A3	CE

AS	CS	SHEET
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+V1.8S +V0.75S +V1.5

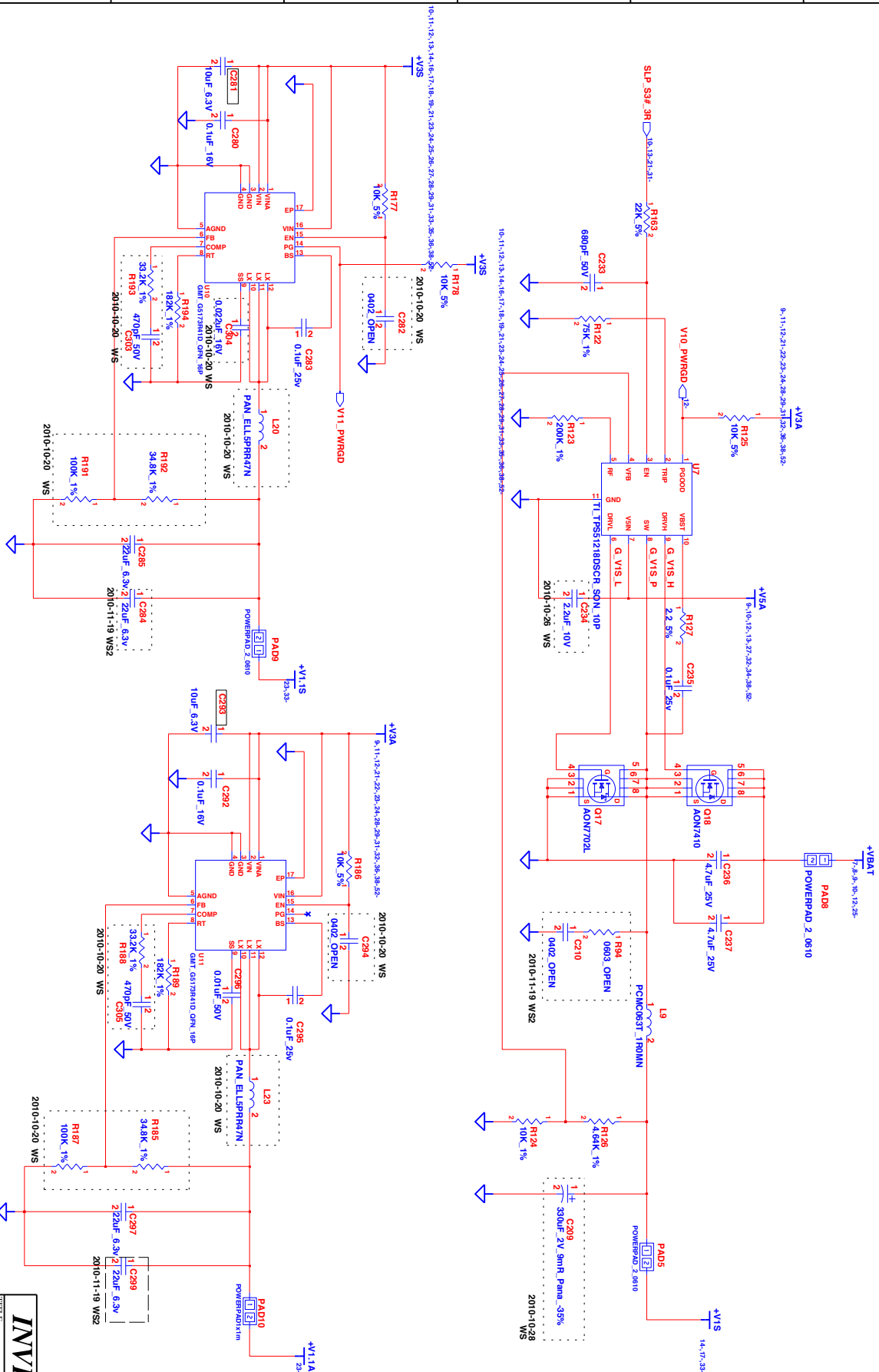


INVENTEC
MIRANDA
POWER

SIZE CODE
A3 CS

DOC NUMBER
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+V1.1A +V1S +V1.1S



INVENTEC

MIRANDA

POWER

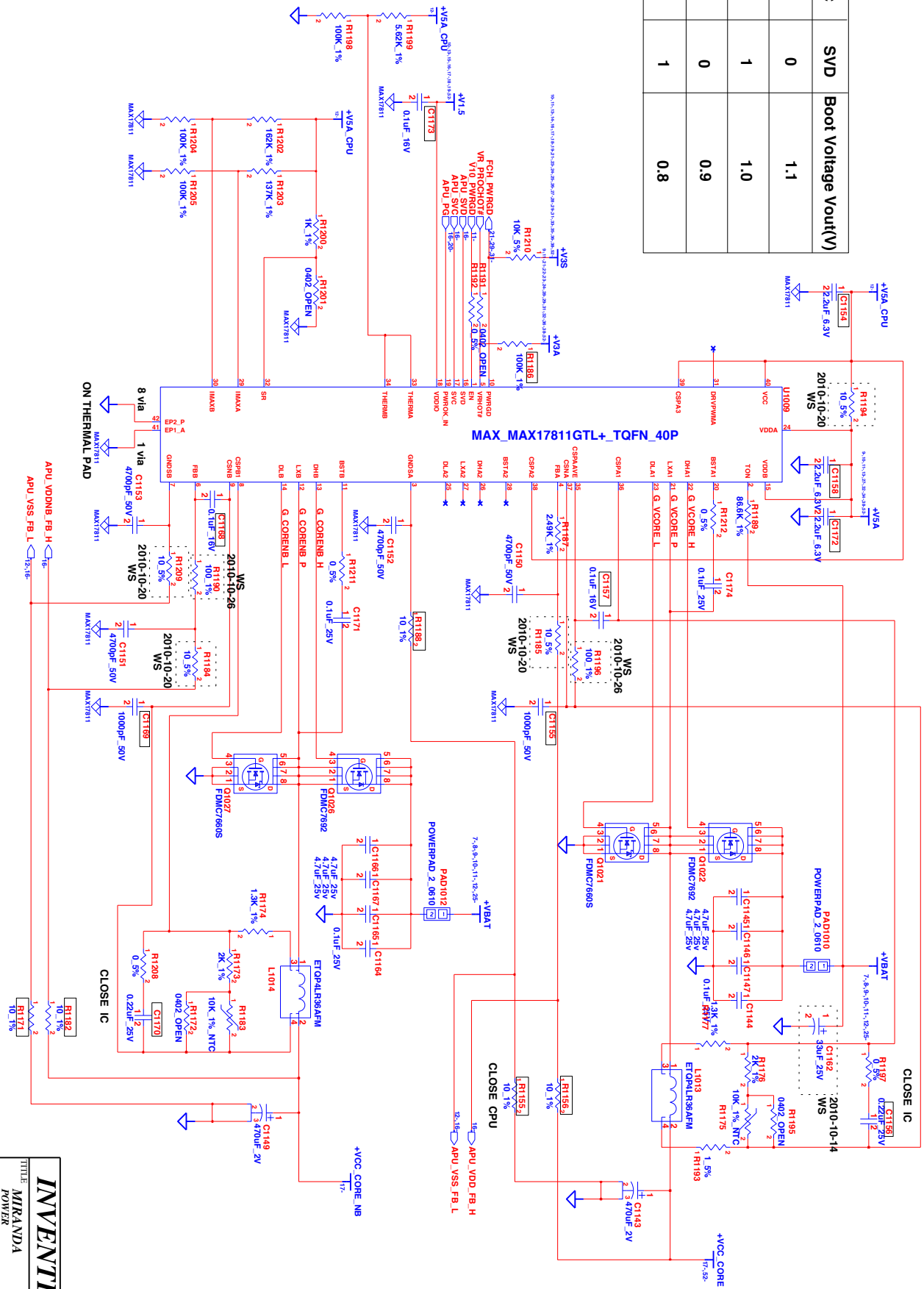
SIZE CODE

AS CS

STRUT

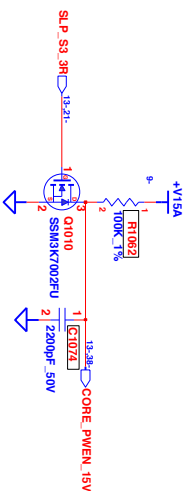
11 OF 54

SVC	SVD	Boot Voltage Vout(V)
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

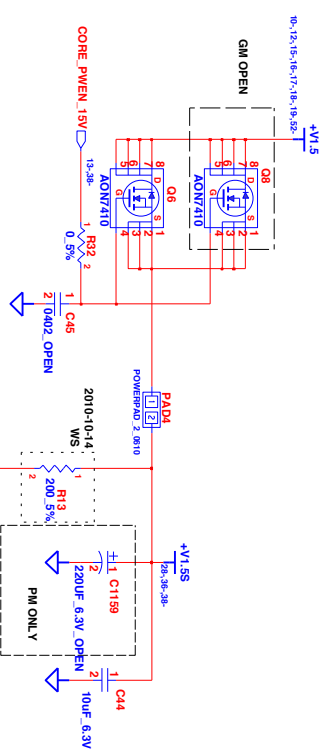
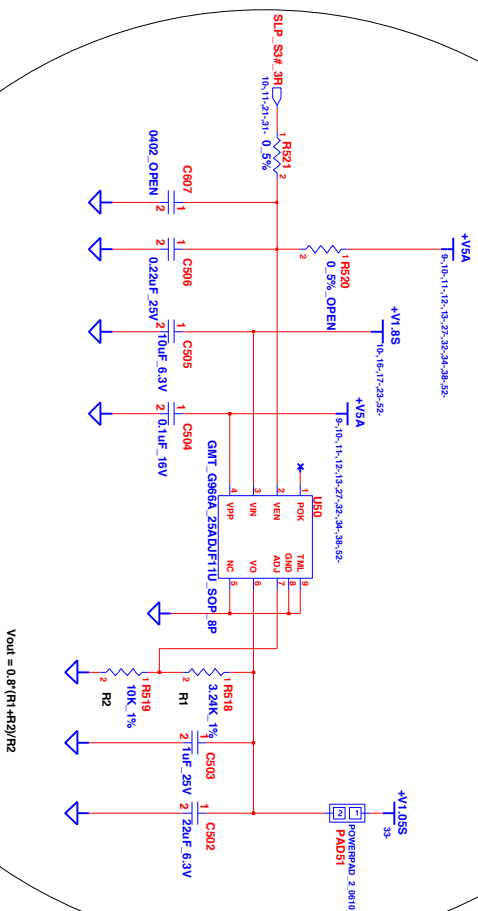


<i>INVENTEC</i>			
TITLE			
MIRANDA			
POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
SHEET	12	OF	54

+V5S +V3S +V1.5S +V1.05S +VAUDIO_5S

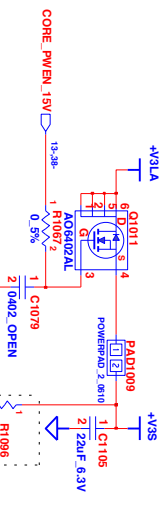


WS2
11/25



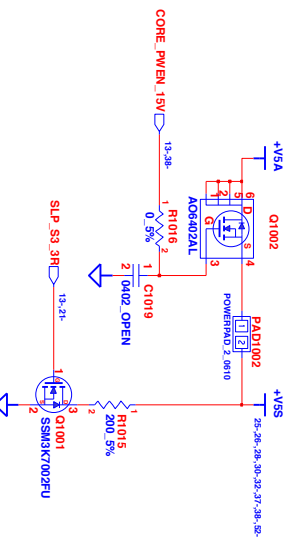
R1062=100K

10-11-12-13-14-15-16-17-18-19-20-21-22-23-24-25-26-27-28-29-30-31-32-33-34-35-36-37-38-39-40-41-42-43-44-45-46-47-48-49-50-51-52-53-54-55-56-57-58-59-60-61-62-63-64-65-66-67-68-69-70-71-72-73-74-75-76-77-78-79-80-81-82-83-84-85-86-87-88-89-90-91-92-93-94-95-96-97-98-99-100



R1062=100K

10-11-12-13-14-15-16-17-18-19-20-21-22-23-24-25-26-27-28-29-30-31-32-33-34-35-36-37-38-39-40-41-42-43-44-45-46-47-48-49-50-51-52-53-54-55-56-57-58-59-60-61-62-63-64-65-66-67-68-69-70-71-72-73-74-75-76-77-78-79-80-81-82-83-84-85-86-87-88-89-90-91-92-93-94-95-96-97-98-99-100



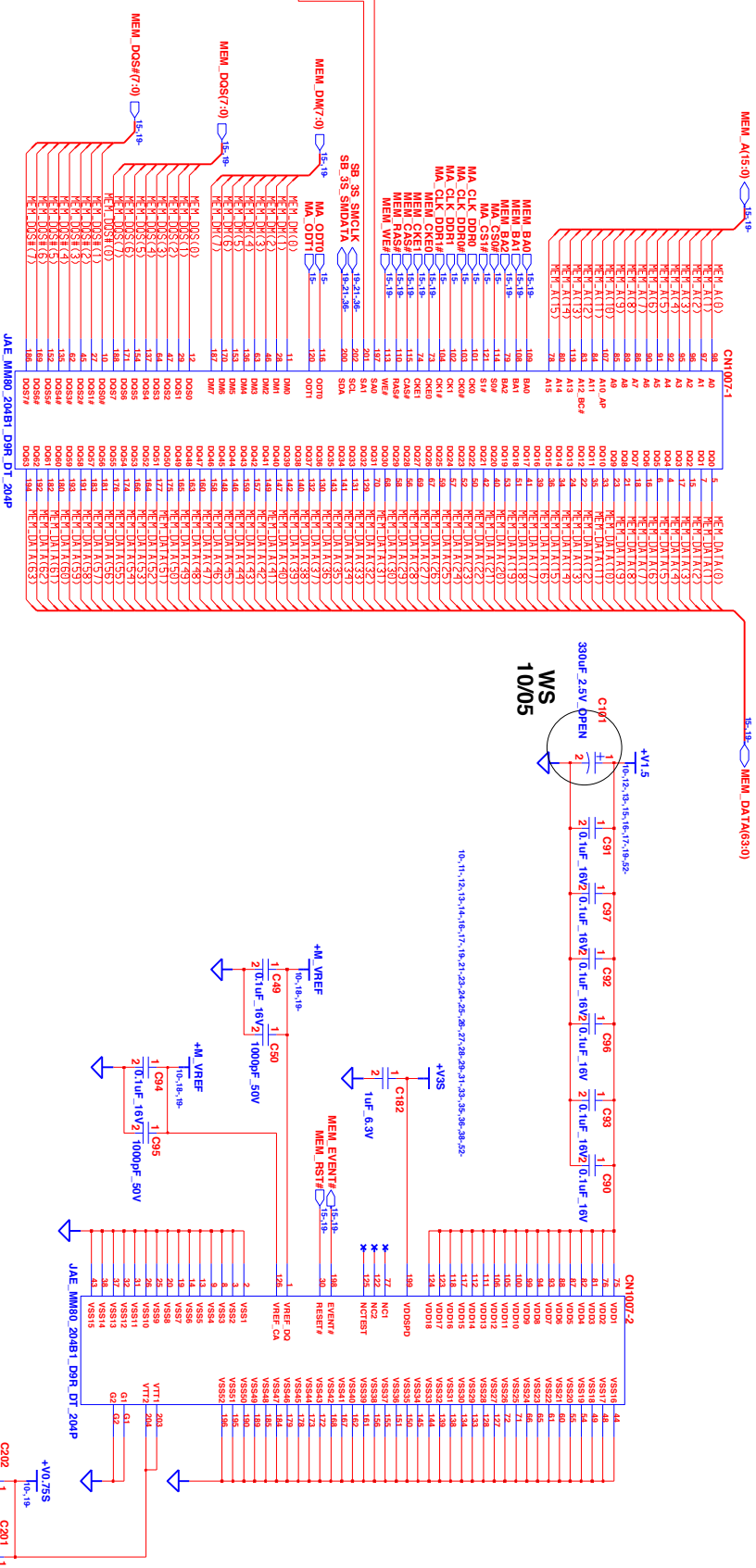
INVENTEC

MIRANDA

SIZE CODE POWER

AS CS 13 OF 54

DDR3



SO DIMMO

SLOT A(REVERSE)

INVENTEC

MIRANDA

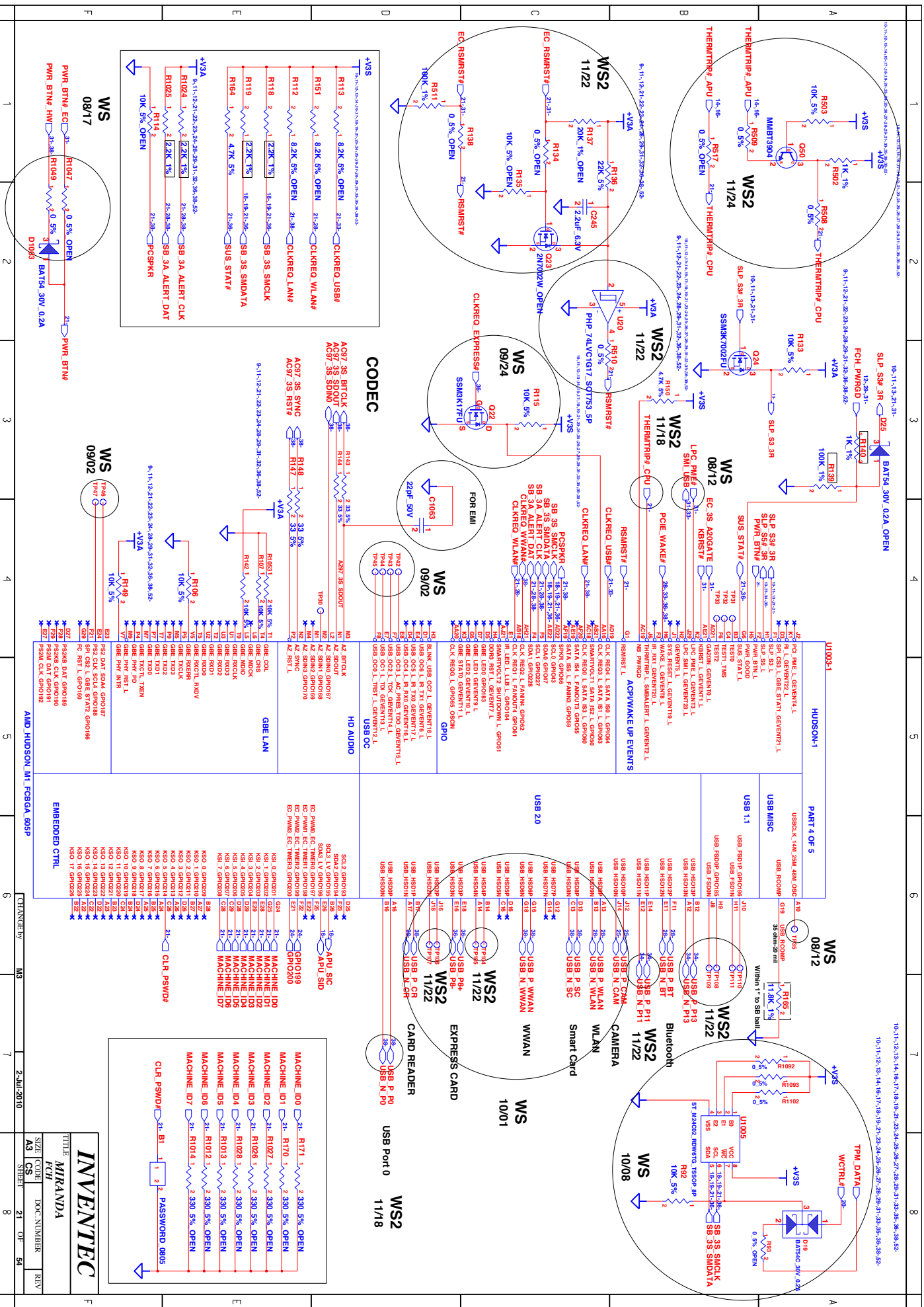
DDR3-J

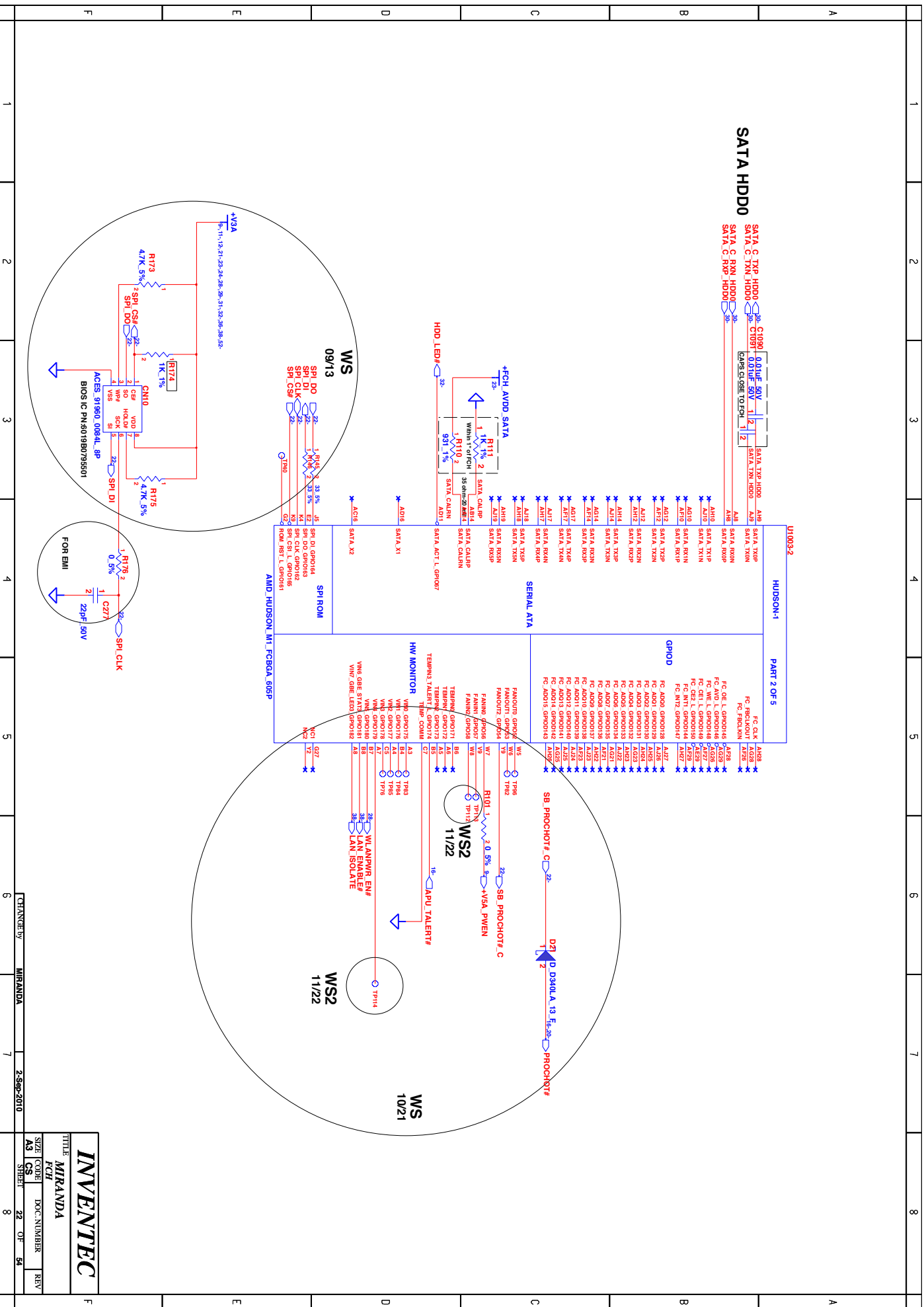
SIZE CODE DOC. NUMBER REV

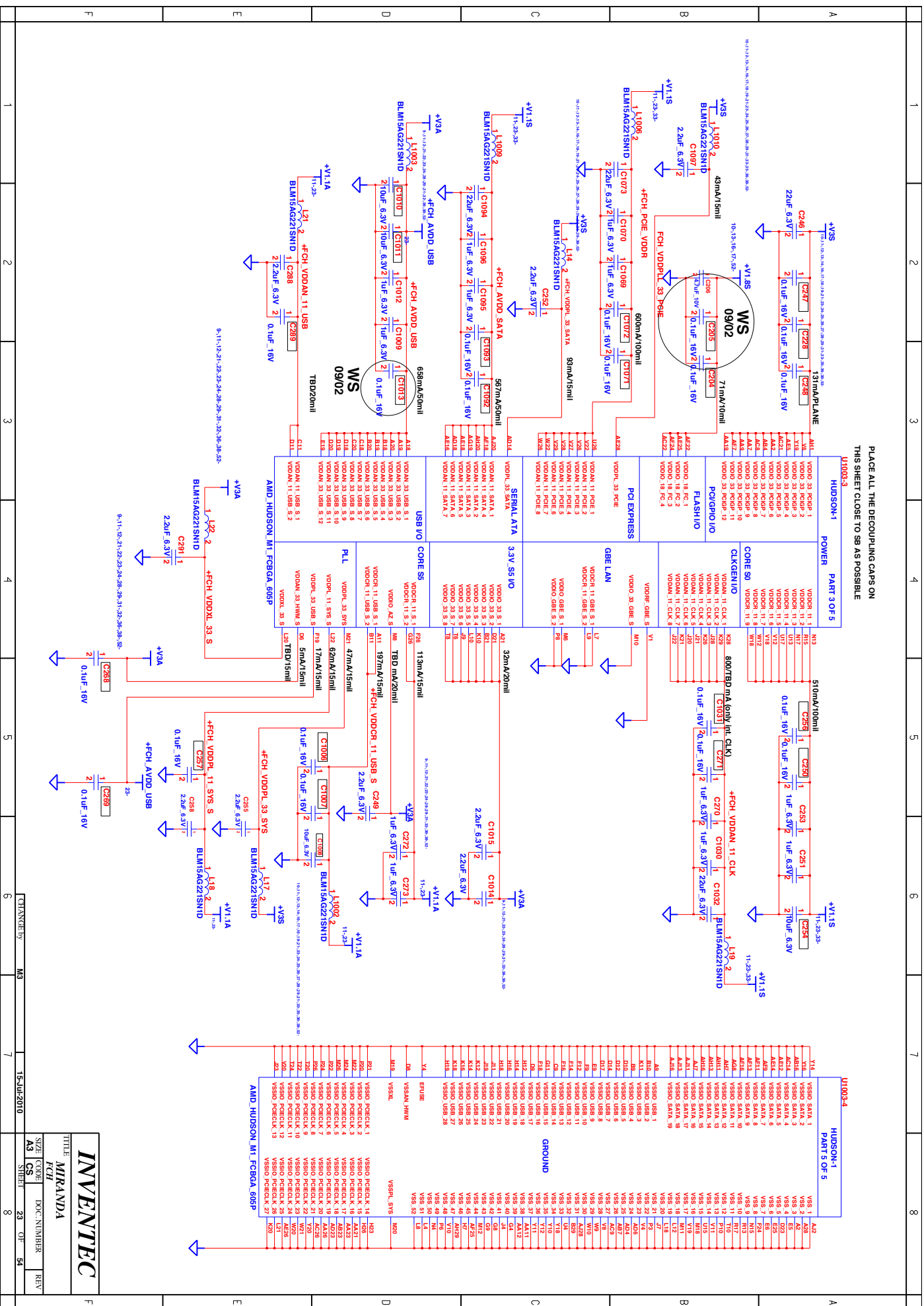
A3 CS 18 OF 54



SLOT B(STANDARD)

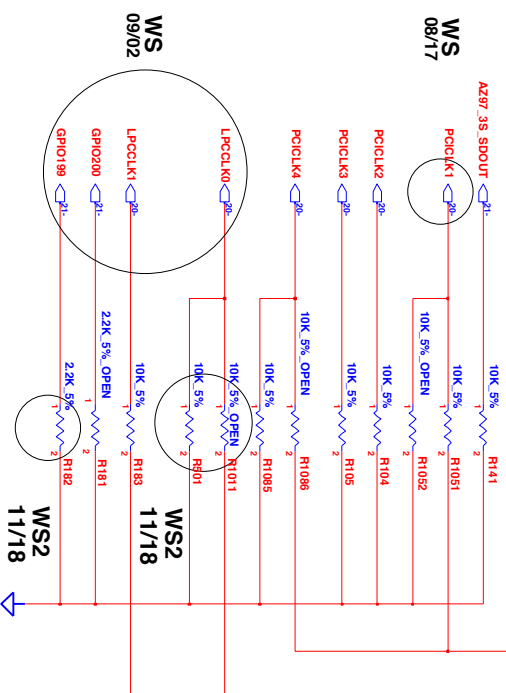






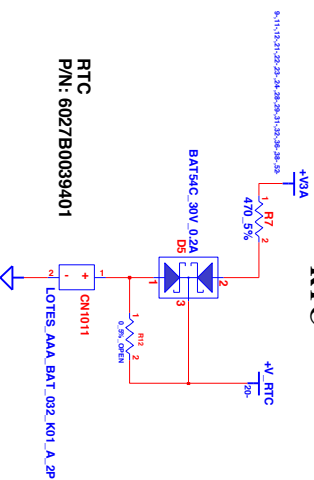
STRAP OPTION / THERMAL / RTC

STRAP PINS

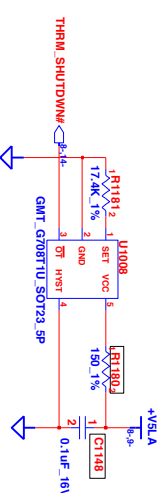


	AZ97_3S SDOUT	PCI CLK1	PCI CLK2	PCI CLK3	PCI CLK4	LPCLK0	LPCLK1	GPIO200 EC_PWM3	GPIO199 EC_PWM2
PULL HIGH	Low Power Mode (Not Supported)	Allow PCIe Gen2 (Default)	Watchdog Timer Enabled (Not Supported)	Debug STRAP Enabled	Non-FUSION CLOCK Mode	Integrated Micro-Controller Enabled (Default)	Internal Clock Generator Enabled (Default)	H.H= Reserved H.L= SPI ROM (Default)	
PULL LOW	Performance Mode (Default)	Force PCIe Gen1 (Default)	Watchdog Timer Disabled (Default)	Debug STRAP Disabled (Default)	FUSION CLOCK Mode (Default)	Integrated Micro-Controller Disabled	Internal Clock Generator Disabled	L.H= LPC ROM L.L= Reserved	

RTC



Thermal Sensor



RTC
P/N: 6027B0039401

INVENTEC

MIRANDA

STRAP OPTION / THERMAL / RTC

SIZE CODE DOC NUMBER REV

A3 CS STRAP 24 OF 34

CHANGED BY MIRANDA

2-SEP-2010

CRT

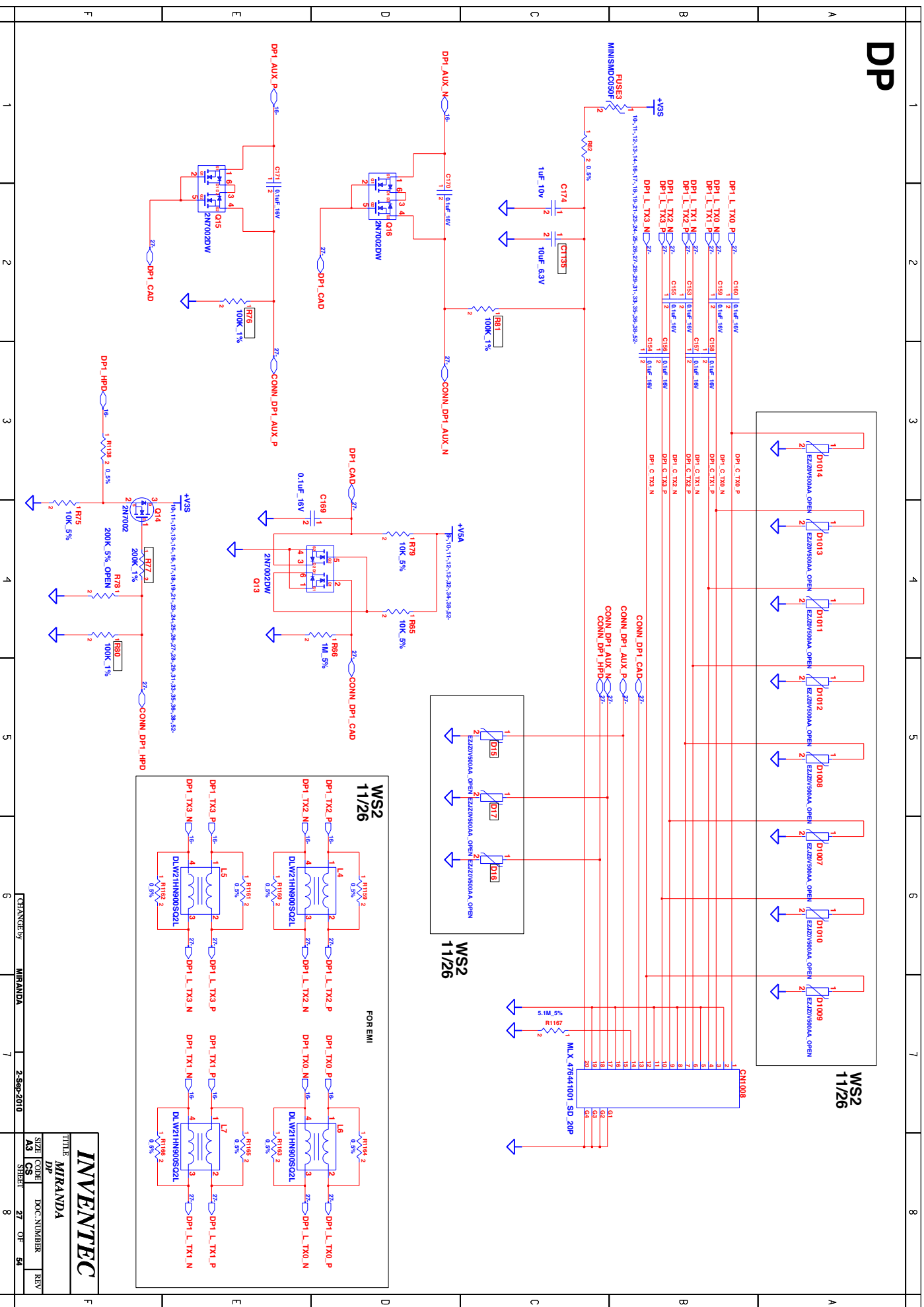
The schematic diagram illustrates the internal circuitry of a CRT monitor, organized into several functional blocks:

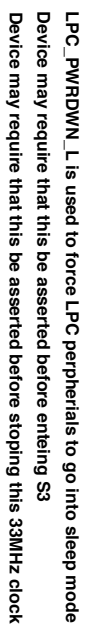
- Power Supply Section:**
 - CRT_DDCCLK_APU:** A 15V power supply line with a 10% tolerance, connected to a 10kΩ resistor (R46) and a 2N7002W MOSFET (Q10).
 - CRT_DDCCLK_APU:** A 15V power supply line with a 10% tolerance, connected to a 10kΩ resistor (R40) and a 2N7002W MOSFET (Q10).
 - CRT_DDCDATA_APU:** A 15V power supply line with a 10% tolerance, connected to a 10kΩ resistor (R46) and a 2N7002W MOSFET (Q10).
 - +VSS:** Ground connection for various components.
 - +VSS SYNC:** Ground connection for the sync signal.
 - +VDD CRT2:** A 1A, 32V, 0467001 diode connected to the +VSS line.
- Video Input Section:**
 - VGA R, DVI:** Video input signals connected to the monitor's input ports.
 - VGA G, DVI:** Video input signals connected to the monitor's input ports.
 - VGA B, DVI:** Video input signals connected to the monitor's input ports.
 - HSYNC, VSYNC:** Horizontal and vertical sync signals connected to the monitor's input ports.
 - HSYNC, VSYNC:** Horizontal and vertical sync signals connected to the monitor's input ports.
 - HSYNC, VSYNC:** Horizontal and vertical sync signals connected to the monitor's input ports.
- Control Logic Section:**
 - U3: PHP 71VC62120DP_TSOP_8P:** A control IC connected to the +VSS, +VSS SYNC, and +VDD CRT2 lines.
 - U4: AZ2305V2:** A control IC connected to the +VSS, +VSS SYNC, and +VDD CRT2 lines.
 - U5: AZ2305V2:** A control IC connected to the +VSS, +VSS SYNC, and +VDD CRT2 lines.
 - U6: AZ2305V2:** A control IC connected to the +VSS, +VSS SYNC, and +VDD CRT2 lines.
 - U7: AZ2305V2:** A control IC connected to the +VSS, +VSS SYNC, and +VDD CRT2 lines.
- Other Components:**
 - R42, R44, R46:** Resistors connected to the power supply lines.
 - R47, R48, R49:** Resistors connected to the video input lines.
 - R50, R51, R52:** Resistors connected to the control logic lines.
 - C76, C77, C78, C79:** Capacitors connected to the power supply and control logic lines.
 - D12, D13, D14:** Diodes connected to the power supply and control logic lines.
 - Q10:** A MOSFET connected to the power supply lines.

CHANGE BY: MIRANDA 13-SEP-2010

TITLE		M3	
SIZE CODE		VGA	
REV		A3 CS	
DATE		28 OF 54	

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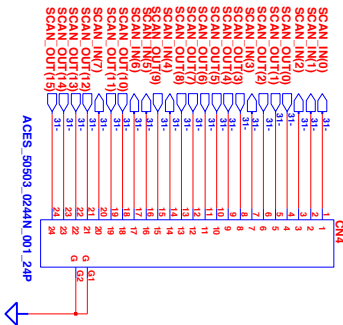




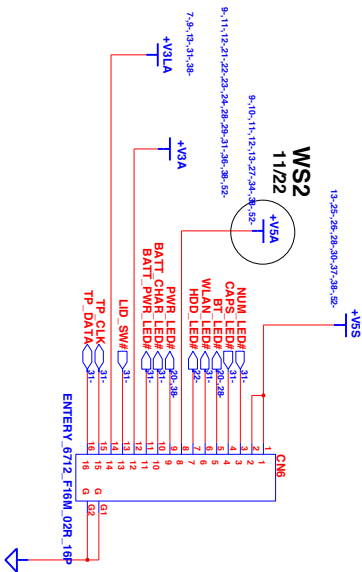
www.vinafix.vn

KB/TTP CONN

Keyboard Conn.



TP/B Conn. M/B SIDE

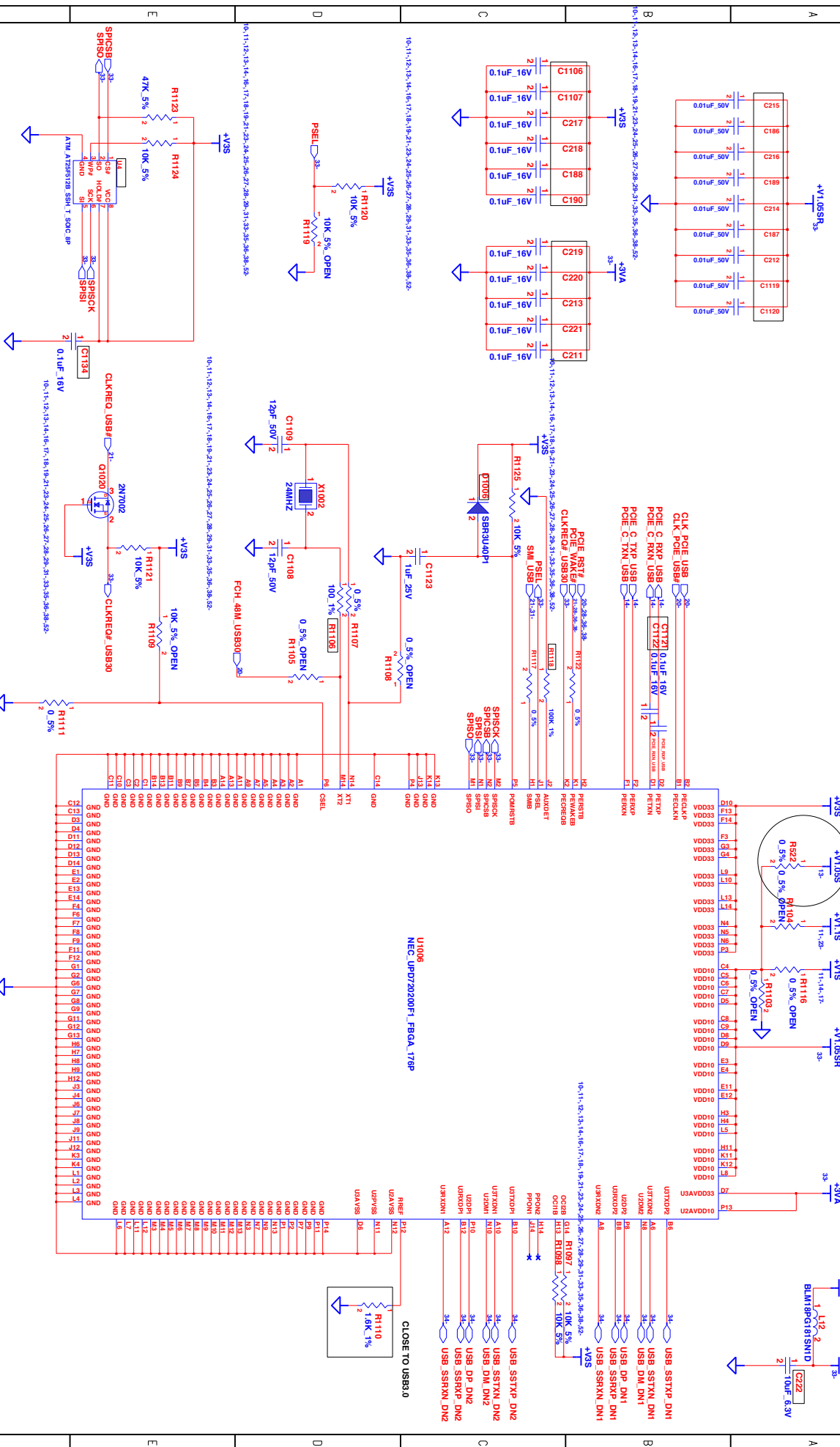


INVENTEC

MIRANDA

SIZE CODE A3 CS
DOC. NUMBER 32 OF 54
REV

USB3.0 CONTROLLER



INVENTEC

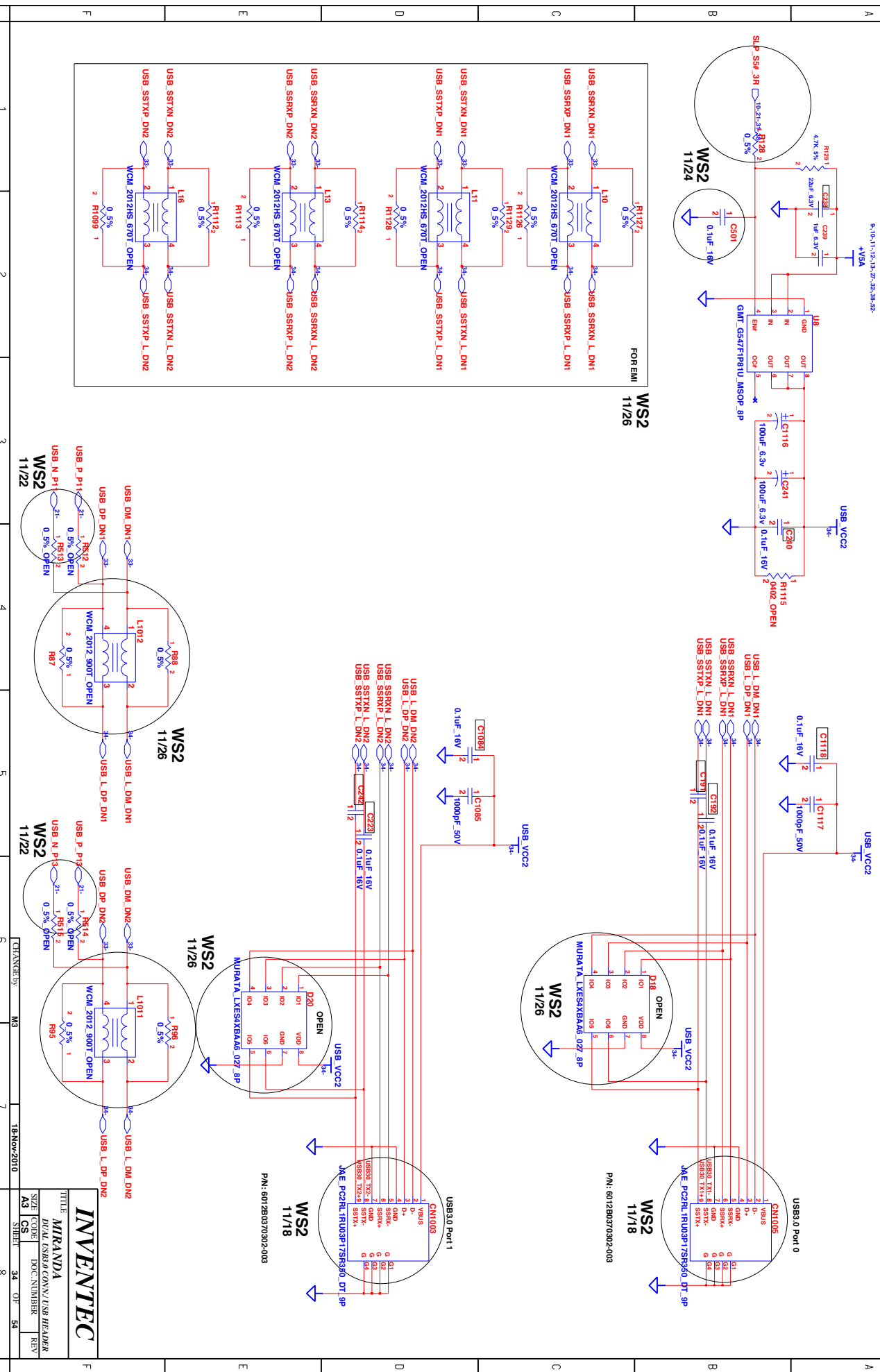
MIRANDA

USB3.0 CONTROLLER

SIZE CODE DOC NUMBER REV

A3 CS 33 OF 54

DUAL USB 3.0 CONN

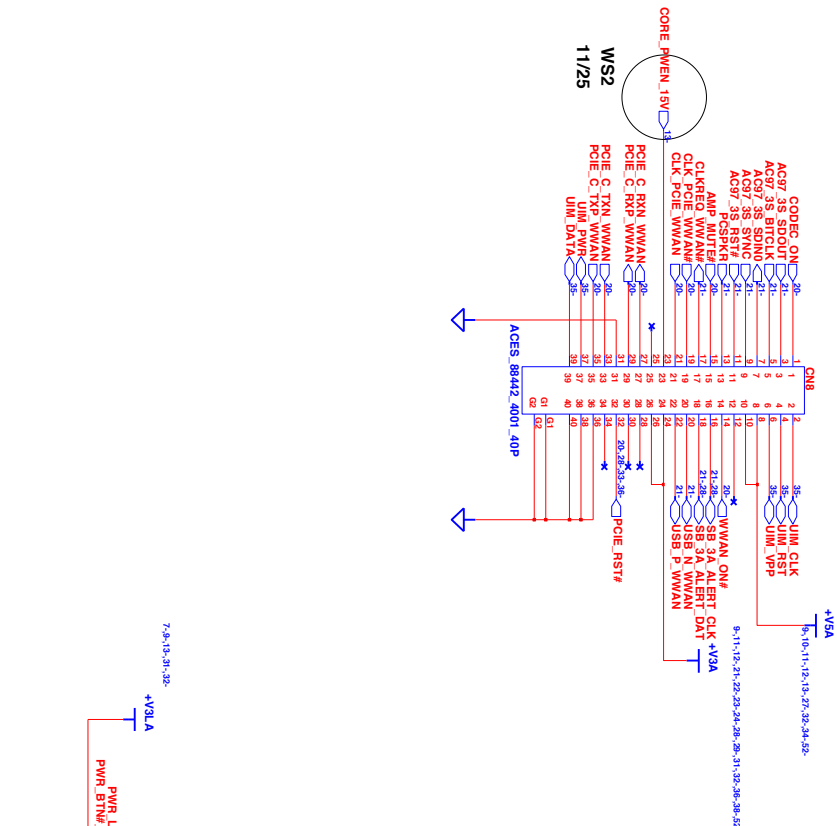


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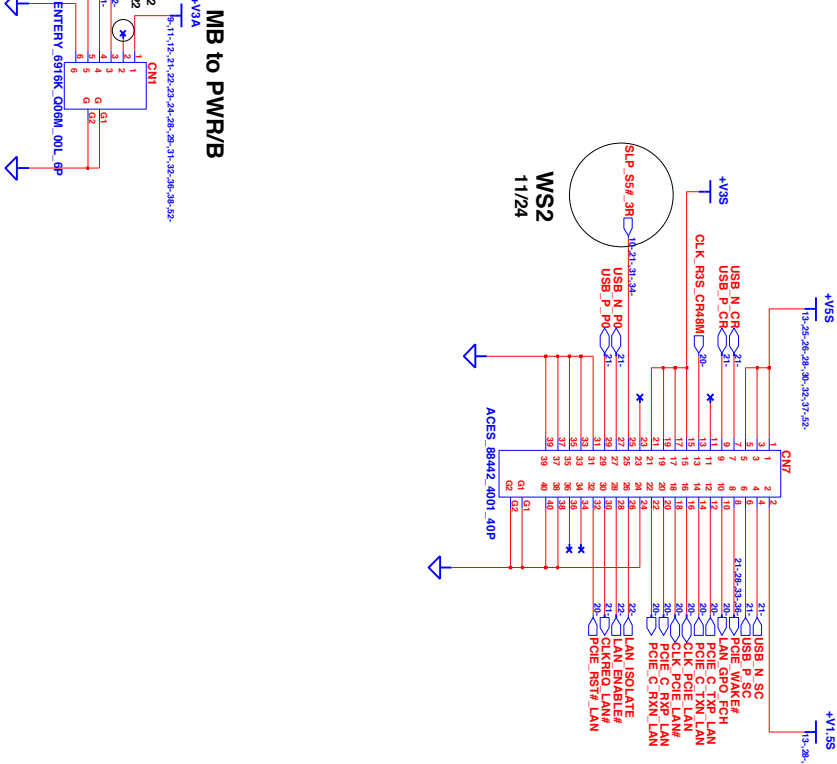
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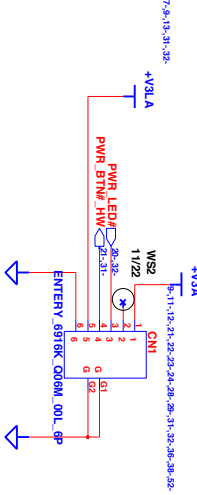
WIRE to BOARD CONN (MB SIDE)



MB to I/O B



MB to PWR/B



INVENTEC

MIRANDA

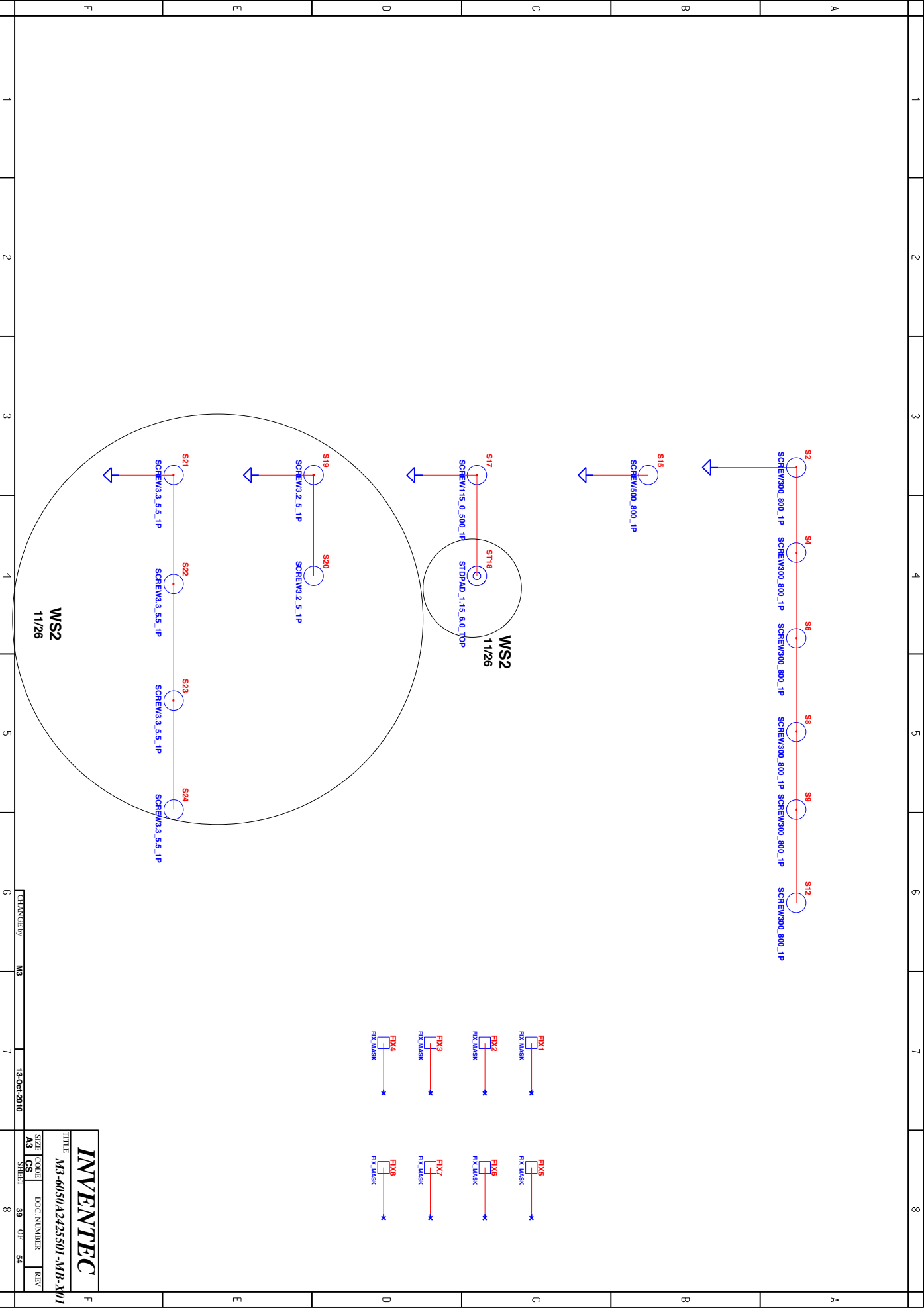
RESER CARD

SIZE CODE

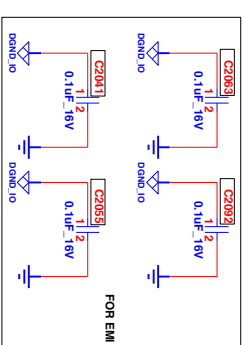
A3 CS

SHR1

38 OF 54



Locate under CODEC
use 80 mils wide trace
bridging AGND and DGND planes



MIRANDA

CODEC

SIZE	CODE	DOC
A3	CS	

SHEET 40

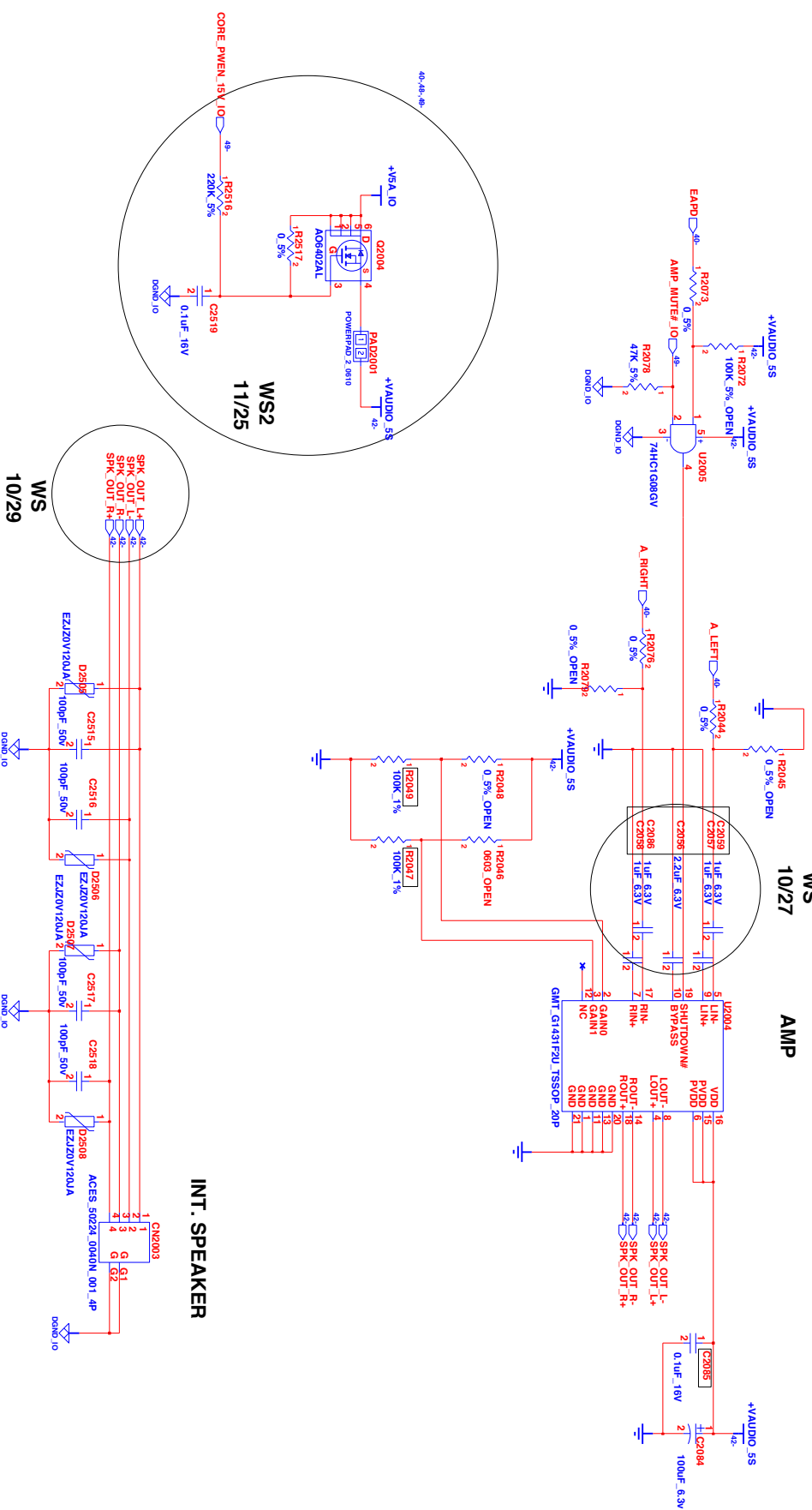
8

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[illegible]

AMP / SPEAKER

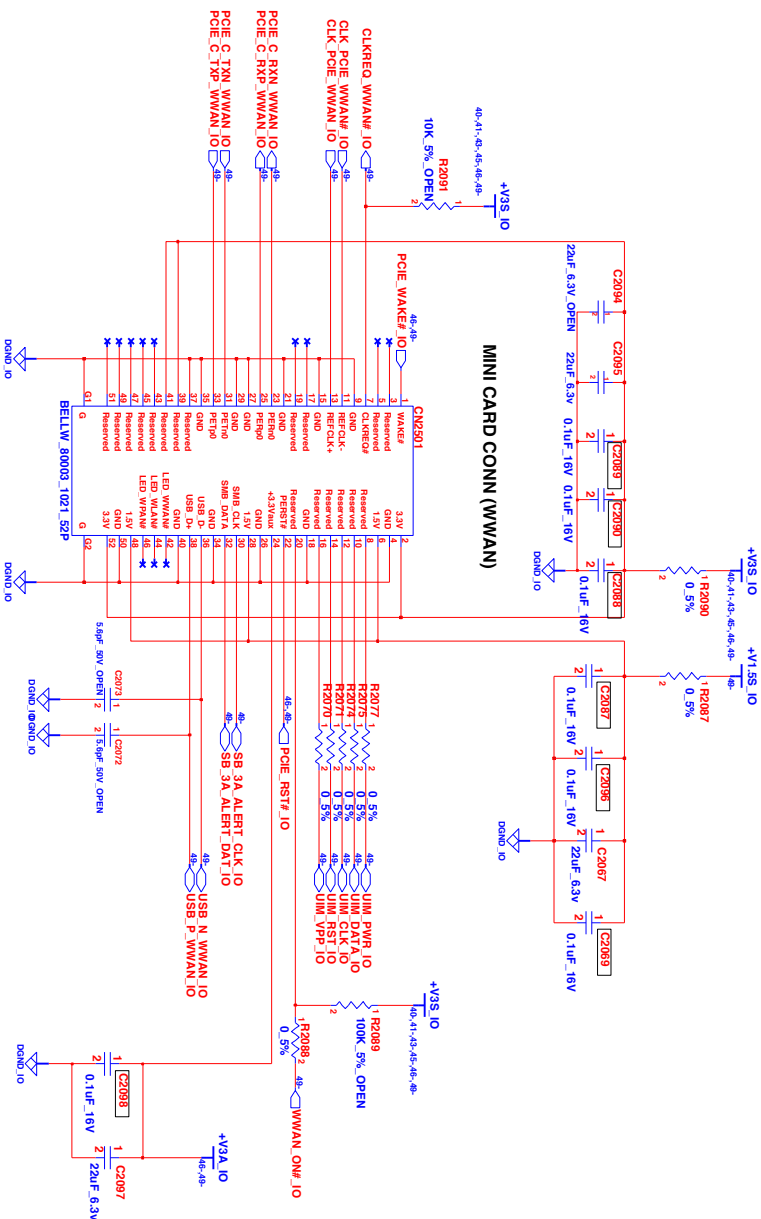
EAPD when output high, turn on OP
EAPD when output low, turn off OP



CHANGE by	M3	23-Jun-2010
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<i>INVENTEC</i>			
TITLE MIRANDA			
AMP/SPEAKER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS		
SHEET	42	OF	54

WWAN



INVENTEC

M3

WWAN

SIZE CODE M3
CS
REV

DOC NUMBER 43 OF 54

[illegible]

[illegible]

USB CARD READER

The schematic diagram illustrates the internal circuitry of the USB Card Reader. It features three main integrated circuits: U2009 (REA RT53128 GRT OFN 24P), U2006 (TAI RD15 A10 LM 42P), and WS2 (11/19). The U2009 chip is connected to a USB N CR IO and a USB P CR IO. It also interfaces with a CARD VCC and a CARD IO. The U2006 chip is connected to a CARD VCC and a CARD IO. The WS2 chip is connected to a CARD VCC and a CARD IO. The diagram shows various signal lines, including SD CLK, MS, DAT0, DAT1, DAT2, DAT3, DAT4, DAT5, DAT6, DAT7, DAT8, DAT9, DAT10, DAT11, DAT12, DAT13, DAT14, DAT15, DAT16, DAT17, DAT18, DAT19, DAT20, DAT21, DAT22, DAT23, DAT24, DAT25, DAT26, DAT27, DAT28, DAT29, DAT30, DAT31, DAT32, DAT33, DAT34, DAT35, DAT36, DAT37, DAT38, DAT39, DAT40, DAT41, DAT42, DAT43, DAT44, DAT45, DAT46, DAT47, DAT48, DAT49, DAT50, DAT51, DAT52, DAT53, DAT54, DAT55, DAT56, DAT57, DAT58, DAT59, DAT60, DAT61, DAT62, DAT63, DAT64, DAT65, DAT66, DAT67, DAT68, DAT69, DAT70, DAT71, DAT72, DAT73, DAT74, DAT75, DAT76, DAT77, DAT78, DAT79, DAT80, DAT81, DAT82, DAT83, DAT84, DAT85, DAT86, DAT87, DAT88, DAT89, DAT90, DAT91, DAT92, DAT93, DAT94, DAT95, DAT96, DAT97, DAT98, DAT99, DAT100, DAT101, DAT102, DAT103, DAT104, DAT105, DAT106, DAT107, DAT108, DAT109, DAT110, DAT111, DAT112, DAT113, DAT114, DAT115, DAT116, DAT117, DAT118, DAT119, DAT120, DAT121, DAT122, DAT123, DAT124, DAT125, DAT126, DAT127, DAT128, DAT129, DAT130, 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For +V3_LAN pins-12, 27, 39, 42, 47, 48

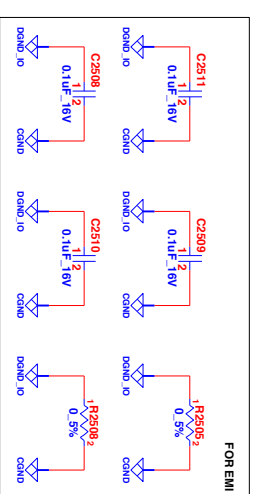
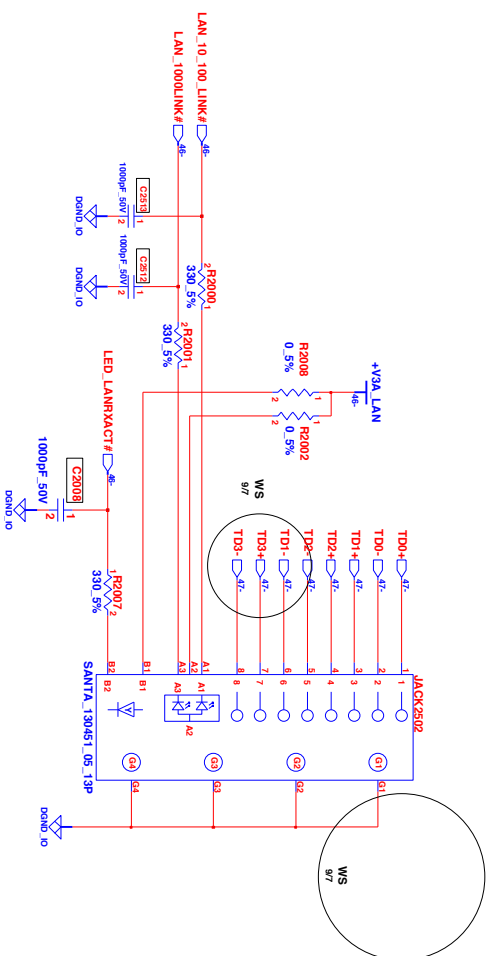
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For VDD10 pins-3, 6, 9, 13, 29, 41, ...

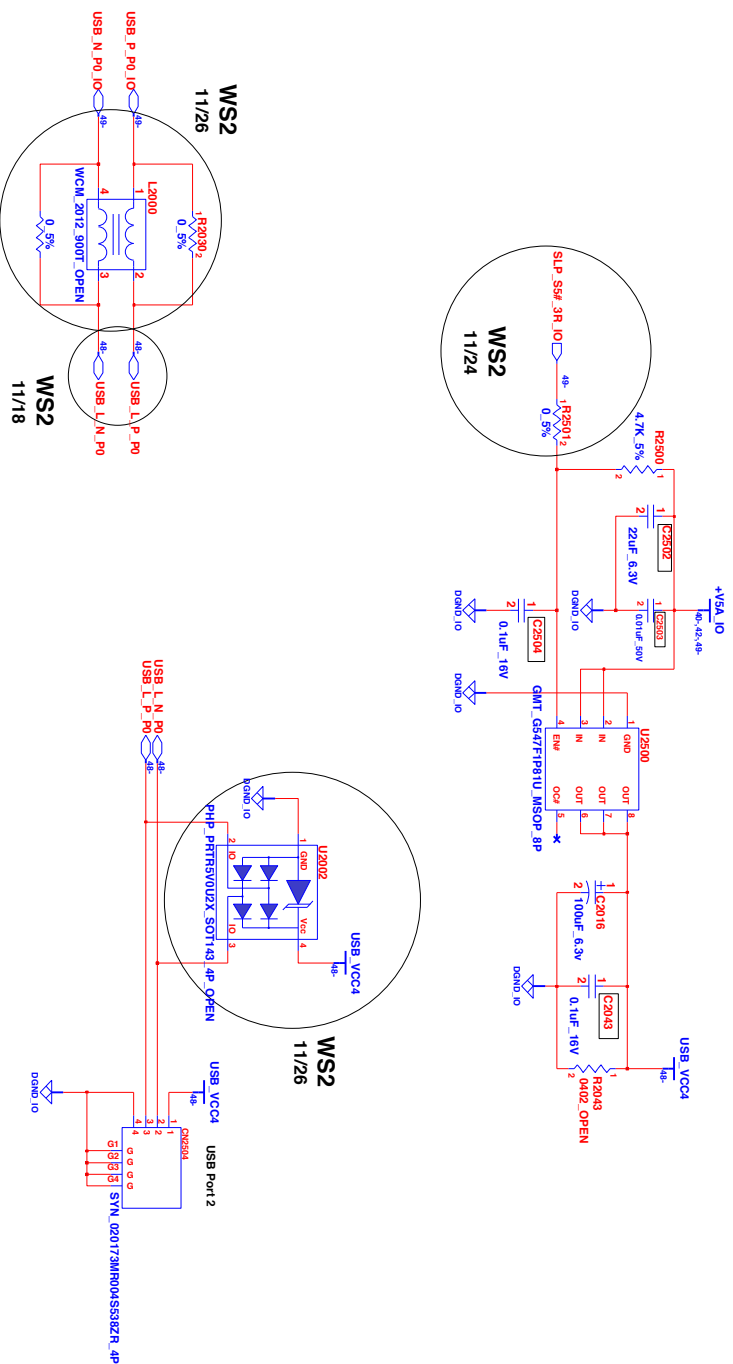
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TITLE			
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SIZE	CODE	DOC. NUMBER	REV.
A3	CS		
SHEET		46	OF 54

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INVENTEC

MIRANDA

LITE Block Diagram

SIZE CODE DOC NUMBER REV
A3 CS 1310AXXX34-017X10

SYN 020173M004SS32ZR, 4P

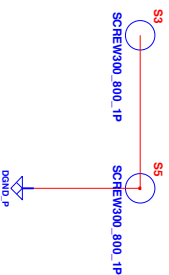
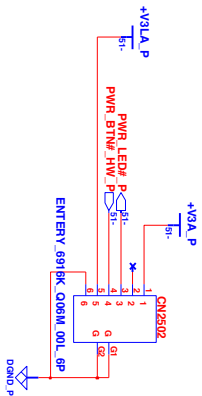
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TOUCH PAD BOARD

The schematic diagram illustrates the electrical layout of the TOUCH PAD BOARD. It includes the following components and connections:

- LEDs and Drivers:** BT_LED#, WLAN_LED#, NUM_LED#, CAPS_LED#, BATT_CHAR_LED#, and BATT_PWR_LED# are connected to their respective drivers (D2001, D2002, D2003, D2004, D2005, D2006) through current-limiting resistors (R2001, R2002, R2003, R2004, R2005, R2006).
- Power and Grounding:** The board is powered by +V3A_T and +V5A_T, with various ground connections (GND, TP, LID_SW#_T, etc.).
- Connectors:** The board features several connectors, including MISAKI_NTC017, MISAKI_NTC017, and TP_BDATA.
- Switches and Relays:** SW2000, SW2001, and SW2002 are used for switching, while EZL20V500AA and EZL20V500AA are used for relaying.
- Resistors and Capacitors:** Various resistors (R2001, R2002, R2003, R2004, R2005, R2006) and capacitors (C2001, C2002, C2003, C2004, C2005, C2006) are used for timing and signal conditioning.
- Legend:** A legend at the bottom left identifies the components and their values:

WLAN_LED#	BT_LED#	NUM_LED#	CAPS_LED#	BATT_CHAR_LED#	BATT_PWR_LED#
PURPLE	BLUE	AMBER	BLUE	BLUE	BLUE



SIZE	CODE	DOC. NUMBER	REV
A3	CS		

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